COMPLIANT



Vishay High Power Products

Schottky Rectifier, 1.0 A





PRODUCT SUMMARY			
I _{F(AV)}	1.0 A		
V_{R}	15 V		

FEATURES

- 125 °C T_J operation (V_R < 5 V)
- Optimized for OR-ing applications
- Ultralow forward voltage drop
- High frequency operation
- Guard ring for enhanced ruggedness and long term reliability
- High purity, high temperature epoxy encapsulation for enhanced mechanical strength and moisture resistance
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- Compliant to RoHS directive 2002/95/EC
- Designed and qualified for industrial level

DESCRIPTION

The VS-10BQ015PbF surface mount Schottky rectifier has been designed for applications requiring low forward drop and very small foot prints on PC boards. The proprietary barrier technology allows for reliable operation up to 125 °C junction temperature. Typical applications are in disk drives, switching power supplies, converters, freewheeling diodes, battery charging, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS						
SYMBOL	CHARACTERISTICS	VALUES	UNITS			
I _{F(AV)}	Rectangular waveform	1.0	А			
V_{RRM}		15	V			
I _{FSM}	t _p = 5 μs sine	140	Α			
V _F	1.0 Apk, T _J = 125 °C	0.32	V			
T _J	Range	- 55 to 125	°C			

VOLTAGE RATINGS				
PARAMETER	SYMBOL	VS-10BQ015PbF	UNITS	
Maximum DC reverse voltage	V_{R}	15	V	
Maximum working peak reverse voltage	V_{RWM}	25		

ABSOLUTE MAXIMUM RATINGS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
Maximum average forward current See fig. 5	I _{F(AV)}	50 % duty cycle at T _L = 84 °C, rectangular waveform		1.0	Α
Maximum peak one cycle non-repetitive surge current	1	5 µs sine or 3 µs rect. pulse	Following any rated load condition and with rated	140	Α
non-repetitive surge current I _{FSM} See fig. 7	10 ms sine or 6 ms rect. pulse	V _{RRM} applied	40	^	
Non-repetitive avalanche energy	E _{AS}	$T_J = 25 ^{\circ}\text{C}, I_{AS} = 1 \text{A}, L = 2 \text{mH}$		1.0	mJ
Repetitive avalanche current	I _{AR}	Current decaying linearly to zero in 1 μ s Frequency limited by T_J maximum $V_A = 1.5 \times V_R$ typical		1.0	А

VS-10BQ015PbF

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ELECTRICAL SPECIFICATIONS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
Maximum forward voltage drop	V _{FM} ⁽¹⁾	1 A	T _J = 25 °C	0.35	V
		2 A		0.44	
See fig. 1		1 A	T _J = 125 °C	0.32	
		2 A		0.40	
Maximum reverse leakage current	I _{RM} ⁽¹⁾	T _J = 25 °C	V _R = Rated V _R	0.5	- mA
See fig. 2	'RM \"/	T _J = 100 °C		12	
Threshold voltage	V _{F(TO)}	$T_J = T_J$ maximum		-	٧
Forward slope resistance	r _t				m $Ω$
Typical junction capacitance	C _T	$V_R = 5 V_{DC}$, (test signal range 100 kHz to 1 MHz), 25 °C		390	pF
Typical series inductance	L _S	Measured lead to lead 5 mm from package body		2.0	nH
Maximum voltage rate of change	dV/dt	Rated V _R		10 000	V/µs

Note

 $^{^{(1)}\,}$ Pulse width < 300 $\mu s,$ duty cycle < 2 %

THERMAL - MECHANICAL SPECIFICATIONS				
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS
Maximum junction temperature range	T _J ⁽¹⁾		- 55 to 125	°C
Maximum storage temperature range	T _{Stg}		- 55 to 150	C
Maximum thermal resistance, junction to lead	R _{thJL} (2)	DC operation See fig. 4	36	°C/W
Maximum thermal resistance, junction to ambient	R _{thJA}	DC operation	80	C/VV
Approximate weight			0.10	g
Approximate weight			0.003	oz.
Marking device		Case style SMB (similar to DO-214AA)	V-	IC

Notes

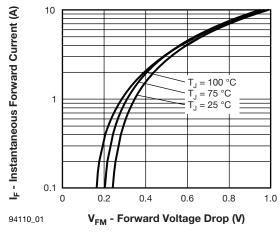
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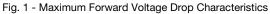
 $[\]frac{dP_{tot}}{dT_J} < \frac{1}{R_{thJA}} \quad \text{thermal runaway condition for a diode on its own heatsink}$

⁽²⁾ Mounted 1" square PCB



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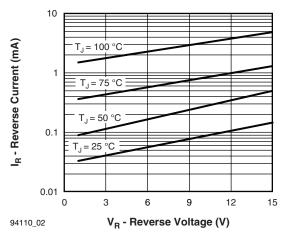


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

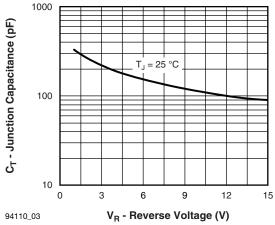


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

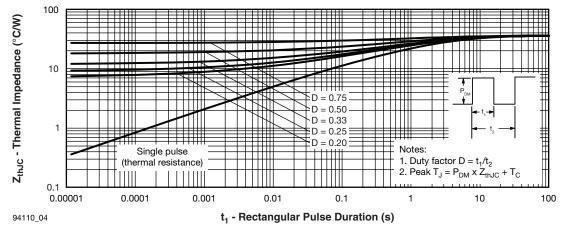
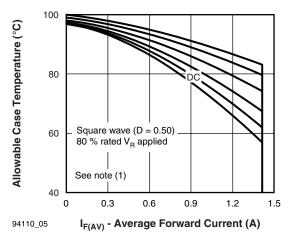


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics (Per Leg)

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D = 0.20D = 0.25Average Power Loss (W) 0.4 D = 0.33D = 0.50D = 0.750.3 RMS limit 0.2 0.1 0 0 0.3 0.6 0.9 1.2 1.5 I_{F(AV)} - Average Forward Current (A) 94110_06

0.5

Fig. 5 - Maximum Allowable Case Temperature vs.
Average Forward Current

Fig. 6 - Forward Power Loss Characteristics

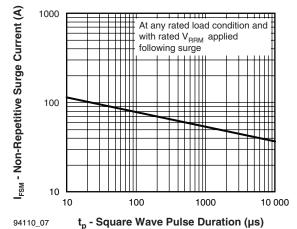


Fig. 7 - Maximum Non-Repetitive Surge Current

Note

(1) Formula used: $T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC}$; $Pd = Forward power loss = I_{F(AV)} \times V_{FM} at (I_{F(AV)}/D)$ (see fig. 6); $Pd_{REV} = Inverse power loss = V_{R1} \times I_R (1 - D)$; I_R at $V_{R1} = 80 \%$ rated V_R

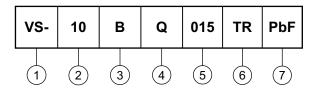
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ORDERING INFORMATION TABLE

Device code



1 - HPP product suffix

2 - Current rating

3 - B = Single lead diode

4 - Q = Schottky "Q" series

Voltage rating (015 = 15 V)

6 - • None = Box (1000 pieces)

• TR = Tape and reel (3000 pieces)

7 - PbF = Lead (Pb)-free

LINKS TO RELATED DOCUMENTS			
Dimensions <u>www.vishay.com/doc?95017</u>			
Part marking information	www.vishay.com/doc?95029		
Dockgoing information	Tape and reel	www.vishay.com/doc?95034	
Packaging information	Bulk	www.vishay.com/doc?95397	
SPICE model		www.vishay.com/doc?95355	



Vishay

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